



IN THE UNITED STATES PATENT & TRADEMARK OFFICE

Re Application of:
CHARLEBOIS ET AL.

Serial No.: 10/709,376

Filed: **APRIL 30, 2004**

Title: **METHOD AND SYSTEM FOR
PERFORMING STATIC TIMING ANALYSIS
ON DIGITAL ELECTRONIC CIRCUITS**

§ Attorney Docket No. **BUR920040056US1**

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Examiner: **UNASSIGNED**

Group Art Unit: **2825**

LETTER TO THE OFFICIAL DRAFTSMAN

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

Submitted herewith are four (4) sheets of replacement formal drawings in the above-referenced patent application.

Respectfully submitted,

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CERTIFICATE OF MAILING

37 C.F.R. § 1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service on the below listed date with sufficient postage for first-class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Date: 3/22/06

By: Vicky Jilgowsky
Signature